1) AND GATE

| A | B | A^B |
| --- | --- | --- |
| T | T | **T** |
| T | F | F |
| F | T | F |
| F | F | F |

OR GATE

| A | B | AνB |
| --- | --- | --- |
| T | T | **T** |
| T | F | **T** |
| F | T | **T** |
| F | F | F |

XOR GATE

| A | B | A⊻B |
| --- | --- | --- |
| T | T | F |
| T | F | **T** |
| F | T | **T** |
| F | F | F |

NOT GATE

| A  INPUT | B  OUTPUT |
| --- | --- |
| T | F |
| F | T |

NAND GATE

| A | B | C |
| --- | --- | --- |
| T | T | F |
| T | F | **T** |
| F | T | **T** |
| F | F | **T** |

NOR GATE

| A | B | A↓B |
| --- | --- | --- |
| T | T | F |
| T | F | F |
| F | T | F |
| F | F | **T** |

XNOR GATE

| A | B | A⊕B |
| --- | --- | --- |
| T | T | **T** |
| T | F | F |
| F | T | F |
| F | F | **T** |

2) (1,1)-->OR GATE=1

3) (0,1)-->XNOR GATE=0

4) A NOT gate has only one input and one output

5) (1,1)-->NAND GATE=0

6) (0,1)-->AND GATE=0

7) (1)-->NOT GATE=0